Electrical and interface properties of Au/DNA/n-Si organic-on-inorganic structures

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A B S T R A C T
The effect of the thickness and coverage rate of a DNA film on the electrical and interface properties of Au/ DNA/n-Si organic-on-inorganic structures has been investigated. The thin film properties of the DNA deposited on n-Si wafer were characterized by atomic force microscopy. The effect of the thickness and coverage rate of the DNA layer was investigated by evaluating electrical parameters, such as the barrier height, ideality factor, series resistance, and interface state density. The thickness and coverage rate of the DNA layer significantly affects the electrical properties of the Au/DNA/n-Si organic-on-inorganic structures. The interface state density properties of the Au/DNA/n-Si diodes were determined by conductance technique. The results show that the interface state density decreases with decrease in both film thickness and coverage rate of the DNA in an acetate buffer, modifying the electronic parameters of the Au/DNA/n-Si diodes.

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1. Introduction

As a nano-device material for molecular biotechnology and nano-electronics, there are many advantages to using DNA such as the ability to control its size through base-pair manipulation and to achieve self-assembly [1–6]. Electrostatic interactions of DNA with positively charged surfaces are frequently considered in the literature [7,8]. A DNA molecule (a genetic information carrier) is a double-stranded negatively charged polymer. DNA adsorption at negatively charged surface using self-assembled molecular monolayer or Peptide Nucleic Acid treatments are also investigated [9]. DNA changes its conformation through environmental interactions with the other components of the living cells [10,11]. Several conduction mechanisms, such tunnelling (single-step super-exchange) [12], and hopping (multistep charge transport) [13], over guanine (G) as the DNA base with the lowest ionization potential were suggested for the electrical transport through DNA molecules. Thus, DNA has been shown to be an insulator, [14–16] a semiconductor, [13,17–19] a good conductor, [6] and a proximity-induced superconductor [20].

Controlling of electronic properties of metal/inorganic diodes is the key to fabricate reproducible metal/organic semiconductor/inorganic rectifying devices. Modifying the interface electronic states of the metal/inorganic semiconductor structure through DNA molecules can be essential for electronic devices for biotechnology related applications. This could lead to an efficient barrier height modification of the metal/semiconductor devices.

In several organic/inorganic Schottky diode studies [21–25], modification of the electronic parameters of the diodes by using various organic materials have been demonstrated. Nicollian and Brews suggested that the interface state density properties of the diodes can be determined using conductance technique [26]. In this work, we have modified the electronic interface states of the n-type Si Schottky diode with a DNA, and analyzed. The interface thin film properties of the DNA in the acetate buffer layer formed on n-Si were characterized by utilizing semi-contact tapping mode atomic force microscopy (TM-AFM).

AFM has been widely used as a powerful research tool in molecular biology for analyzing the structures of bio-molecules such as proteins [27–29], DNA [30–32] and others [33,34]. AFM images sample surfaces by continuous scanning with a sharp tip. In simple contact mode AFM, a tip at the end of a flexible cantilever is used to detect sub-nanometer size changes in the interactional force as a function of height for the molecular surface, resulting in a 2-dimensional plot of topographic features [35,36]. In semi-contact tapping mode atomic force microscopy (TM-AFM), the topographic image is collected from any type of (hard or soft) surface with an oscillating probe tip driven near its resonance frequency by a piezoelectric transducer that provides a driving force of a constant amplitude [37,38]. During TM-AFM scanning, an oscillating tip comes so close to the sample surface that, it regularly touches the surface for a very short period of time during its oscillations. The short tip-sample contact time prevents any irreversible destruction on soft surfaces. The relatively weak tip-sample interaction becomes stronger as the tip approaches the surface and it lowers the vibrational amplitude by creating a phase shift between measured and actuating signal [39]. The AFM electronic feedback system maintains the amplitude of...
tip oscillations at a user-defined set point (the operating amplitude) during construction of the surface topography. Obtaining an image of the phase signal simultaneously with the topographical mapping of the sample surface (known as the phase contrast method) provides additional information on the details of the surface structure. While the cantilever operating in the tapping mode gives topographical information via Van der Waals interactions, i.e., a combination of long-range attractive and short-range repulsive forces [40,41], the phase image allows mapping of the variation of surface properties such as composition, adhesion, friction, and visco-elasticity [42,43].

In the present study, the effect of the thickness and coverage rate of the DNA on the acetate (ABS) buffer layer on the electronic interface states of Au/DNA/n-Si organic-on-inorganic Schottky diodes was investigated by evaluating electrical parameters such as the barrier height ($\phi_b$), ideality factor ($n$), series resistance ($R_s$), and interface state density ($D_{it}$) from conductance and capacitance measurements.

2. Experimental details

In this work, an n-type (N/Phos) single crystal silicon wafer pre-polished on one side and having a (1 0 0) surface orientation, thickness of 0.530 mm, diameter of 100 mm and 2.00 Ohm-cm resistivity was purchased from Si-Mat Silicon Wafers Company, and used as a substrate. The Si wafer was cleaned for 10 min in boiling tri-chloroethylene, acetone, and ethanol, consecutively. Then, it was cleaned using the following series of solvent or solvent mixtures: H$_2$SO$_4$, H$_2$O$_2$, HF: H$_2$O (1:20) solution, HNO$_3$:HF:H$_2$O (6:1:35), and finally a HF: H$_2$O (1:20) solution. The Si wafer was rinsed thoroughly in de-ionized water of 18 M before and after cleaning with each solvent or solvent mixture. The fish sperm DNA (as lyophilized powder) was obtained from Serva Company (Germany). The double-stranded DNA (dsDNA) stock solution (1000 mg/L) was prepared with ultra pure distilled water and kept frozen. More dilute solutions of dsDNA were prepared in 0.05 M acetate buffer solutions, each containing 20 mM NaCl (ABS, pH 4.80). The DNA sample was diluted to 10 and 0.1 mg/ L with the ABS solution. Immediately after surface cleaning, 6 mL of the DNA solutions at three different concentrations were coated by the drop casting method and allowed to dry 12 h in a humid-free desiccator. The DNA organic film thickness and homogeneity depends on the solution concentration and wetting property on the n-type Si substrate. Using the AFM scratch and measure method, average DNA film thicknesses of 200 and 50 nm for DNA volumetric concentrations of 10 and 0.1 mg/L, respectively were determined for the samples.

In a turbo pumped high vacuum system at a pressure of $5 \times 10^{-6}$ Torr, Au (99.9% purity) with a thickness of 200 nm was thermally evaporated from a tungsten filament using a mask of 2 cm diameter to form the top contact on the DNA film surface of the Si wafer. The back side of the Si substrate had been polished with high speed rotating sand paper to remove the native oxide and to create an ohmic contact before the 200 nm Au thin film deposition. The current–voltage ($I$–$V$) characteristics of the Au/DNA/n-Si diodes were performed with a Model 2400 KEITHLEY (Cleveland, USA) source-meter and a GPIB data transfer card for current–voltage measurements. The capacitance–voltage measurements were measured using a Model 3532 HIOKI HITESTER LCR meter (Bohemia, USA).

DNA samples were visualized with a Solver P47H atomic force microscope (NT-MTD, Moscow, Russia) operating in the tapping mode in air at room temperature. A scanner, equipped with a scanning piezoelectric element (piezo scanner) with maximum scan range of 4 µm × 4 µm × 1.5 µm was used to obtain both surface morphology and phase images of the DNA films that had been coated by the drop casting method on a n-type Si substrate. Special diamond-like carbon (DLC) coated NSG01_DLC silicon cantilevers (NT-MTD) with a 2 nm tip apex curvature, a length of 130 µm, a spring constant of 5.5 N/m, and a resonance frequency of 150 kHz was used to take the topographies during the AFM scanning. The scan resolution was 512 × 512 pixels. The images were processed by the linear flattening method in order to remove the background slope. The Nova 914 software package was used for controlling the SPM system and analyzing the AFM images.

3. Results and discussion

3.1. AFM results of the DNA thin films deposited on Si substrate

In this work, the AFM tapping mode phase imaging (TM-AFMPI) was used to distinguish the DNA molecular film layer from the ABS buffer. When imaging DNA molecules by AFM on any smooth surface, the DNA concentration has to be prepared so low that the individual DNAs can be found easily on the scanned surface separated from each other. In our case, two different concentrations have been prepared to investigate film formation properties of the DNA in ABS buffer on n-type Si wafer surface, rather than visualizing the individual DNA molecules. Nevertheless around the edge of the drop casted DNA film, the individual DNA molecules could be seen separately. We have experienced that the DNA can not be imaged unless it is dried following the adsorption process. It aggressively interacts with the tip and changes the tip resonance frequency by loading extra mass as a result of sticking to the tip. But a few DNA molecules suspended on the tip enhances the interaction between the surface and the tip. The resolution of both topography and especially the phase image increases dramatically after a few scans on the DNA covered film surface.

The AFM topography and AFM phase image of the 200 nm DNA film with the volume concentration of 10 mg/L on the ABS buffer surface are given in Fig. 1a and b, respectively. The color bars on the right sides show surface height and phase angle changes, respectively. The lighter regions have higher altitude on the 2D surface. On the other hand, the lighter regions on the phase image show that more repulsive interaction takes place between the tip and the DNA surface. The topography shows a smooth and completely covered DNA film surface, while the film disorders and pinholes of the DNA film can be distinguished from the contrast of the AFM phase image clearly. The DNA film thickness on ABS buffer layer was measured around 3 nm by taking a cross sectional height profile from one of the pinholes on the film surface as shown in Fig. 1c. The coverage rate of DNA film was obtained as 96% over the area of 3 µm × 3 µm of the film from the statistical calculations of the phase image as given in Fig. 1d.

Fig. 2a and b show the AFM topography and AFM phase image of the 50 nm DNA film with the volume concentration of 0.1 mg/L, respectively. The film disorders and coverage rate is more pronounced in both the topography and phase image. The lighter region represents the DNA film, while the darker region represents the underlying ABS buffer layer. The DNA film thickness on ABS buffer layer has been measured around 4 nm by taking a height profile across a line through edge of a pinhole on the DNA film surface as shown in Fig. 2c. In Fig. 2d, the statistical phase angle–frequency profile of the phase image shows 60% darker regions (buffer layer). The remaining 40% coverage can be deduced for the DNA film coverage over the area of 3 µm × 3 µm of the dried DNA film concentration of 10 and 0.1 mg/L.

3.2. The current–voltage characteristics of Au/DNA/n-Si diodes

Fig. 3 shows the current–voltage characteristics of the Au/DNA/n-Si diodes. The diodes show a rectifying behavior. The rectifying property of the Au/DNA/n-Si diode is improved with film thickness.
The current–voltage characteristics of the diodes can be analyzed by the following relation [44],

\[ I = I_o \exp \left( \frac{q(V - I R_s)}{nkT} \right) \left[ 1 - \exp \left( - \frac{q(V - I R_s)}{kT} \right) \right] \]

where \( n \) is the ideality factor, \( k \) is the Boltzmann constant, \( R_s \) is the series resistance, \( V \) is the applied voltage, \( T \) is the temperature and \( I_o \) is the reverse saturation current. The ideality factors of the diodes were determined from the \( I-V \) characteristics. The ideality factors are higher than unity due to the DNA film with ABS layer and possible thin oxide layer and series resistance at the interface. The oxide layer at the interface may be formed during surface preparation of the Au evaporation. The presence of interfacial layer, series resistance effect and bulk resistance of the DNA film affects the electronic parameters of the organic–inorganic structures. In order to analyze the series resistance effect, we have used Cheung’s method to determine the diode parameters with the Cheung’s functions expressed as [45],

\[ \frac{dV}{d \ln(I)} = \frac{n}{q} kT + I R_s \]
and

\[ H(I) = V - n \frac{kT}{q} \ln \left( \frac{I_e}{A^* A T^*} \right) = IR_s + n \phi_b \]  

(3)

where \( I_e \) is the saturation current, \( A \) is the contact area, \( A^* \) is the Richardson constant (112 A cm\(^{-2}\) K\(^{-2}\) for n-Si) [46], \( R_s \) is the series resistance and \( \phi_b \) is the barrier height. The plots of \( dV/d\ln I \) vs. \( I \) and \( H(I) \) vs. \( I \) of the diodes are shown in Fig. 4a and b and the obtained parameters are given in Table 1. The \( R_s \) and \( n \) values were calculated from the slope and intercept of the \( dV/d\ln I \) vs. \( I \) plot and the results are given in Table 1. The series resistance is decreased with decreasing film thickness. This suggests that the low value of series resistance obtained for the diodes with thinner DNA layer gives a Schottky diode with better performance. The ideality factor and barrier height gives a Schottky diode with better performance. The ideality factor and barrier height. Furthermore, at intermediate and higher voltages, the organic layer may change the charge transport mechanism of the diode. Thus, to be sure about this, \( I-V \) characteristics of the diodes were plotted in logarithmic scale, as shown in Fig. 5. The logarithmic \( I-V \) curves in the figures indicate three different current regions exhibiting the power law behavior of \( I \sim V^n \), where \( m \) shows the slope of each region. The \( m \) values for regions I, II and region III of the Au/DNA (200 nm)/n-Si and Au/DNA (50 nm)/n-Si were found to be 1.08, 8.91, 3.01 and 1.34, 10.48, 2.03, respectively. The obtained \( m \) values correspond to ohmic, trapped charge-limited current (TCLC), and space charge-limited current (SCLC) mechanisms. The third region for the diodes is transformed from TCLC to SCLC mechanism with decrease in DNA film thickness. In the TCLC mechanism, the current is controlled by exponential distribution of traps in the band gap of the DNA layer. The obtained results reveal that the film thickness and coverage rate of the DNA layer modifies the \( I-V \) characteristics of the diode as a result of various charge related conduction mechanisms in the Au/DNA/n-Si diode.

3.3 Interface state density properties of the Au/DNA/n-Si diodes

The plots of capacitance vs. frequency under bias voltages of 0 and 0.5 V are shown in Fig. 6. The capacitance decreases with increasing frequency and tends to non-dispersive. At lower frequencies, the capacitance increases with decreasing frequency. This is an indication of presence of a continuous distribution of the interface states. The non-dispersive behavior of capacitance suggests that the interface states in equilibrium with the semiconductor do not contribute to the capacitance. In the non-dispersive region, the charges at the interface states cannot follow the fast alternating current signal. The capacitance at lower frequencies corresponds to the sum of space-charge capacitance and interface capacitance, while at higher frequencies the total capacitance arises mostly from the space-charge capacitance [43].

For the conductance model, the parallel capacitance \( C_p \) and conductance \( G_p \) relations are expressed as [45],

\[ C_p = C_s + \frac{C_m}{1 + (\omega \tau_s)^2} \]

(4)

and

\[ \frac{G_p}{\omega} = \frac{q \omega \tau_s D_{it}}{1 + (\omega \tau_s)^2} \]

(5)

where \( C_s = qD_{it}, \tau_s = R_s C_s \) and \( \tau_s \) is the interface trap constant. The normalized conductance is expressed as,

\[ \frac{G_p}{\omega} = \frac{q \omega \tau_s D_{it}}{2 \tau_s} \ln(1 + \omega^2 \tau_s^2) \]

(6)

where \( D_{it} \) is the density of the interface states, \( q \) is the charge of the electron, \( \omega \) is the angular frequency, \( \tau \) is the time constant of the interface states. The parallel conductance between measured conductance and capacitance is expressed as following,

\[ \frac{G_p}{\omega} = \frac{\omega C_m C_{ox}^2}{C_m + \omega^2 (C_{ox} - C_m)} \]

(7)

where \( C_{ox} \) is the capacitance of oxide layer, \( C_m \) and \( C_{ox} \) are measured conductance and capacitance respectively. The interface state density is determined using the following relation,

\[ D_{it} = \left( \frac{G}{\omega} \right)_{\text{max}} \]

(8)

Fig. 7 shows the plots of \( (G/\omega) \) vs. \( f \) of the diode at different bias voltages. The plots indicate a peak due to the presence of interface states. The shift in the peak position to higher frequencies with
Table 1
Electronic parameters of the diodes.

<table>
<thead>
<tr>
<th>Diode</th>
<th>( n )</th>
<th>( eV \phi_b ) (eV)</th>
<th>( eV \phi_h ) (eV)</th>
<th>( dV/d\ln I ) (( \Omega ))</th>
<th>( H/I ) (( \Omega ))</th>
<th>( D_{it} ) (eV cm(^{-2}))</th>
</tr>
</thead>
<tbody>
<tr>
<td>Au/DNA(200 nm)/n-Si</td>
<td>1.41</td>
<td>1.35</td>
<td>0.87</td>
<td>41.84</td>
<td>42.51</td>
<td>( 8.56 \times 10^{12} )</td>
</tr>
<tr>
<td>Au/DNA(50 nm)/n-Si</td>
<td>1.35</td>
<td>1.17</td>
<td>0.75</td>
<td>13.23</td>
<td>11.19</td>
<td>( 2.54 \times 10^{12} )</td>
</tr>
</tbody>
</table>

Fig. 4. Plots of \( dV/d\ln I \) vs. \( I \) and \( H(I) \) vs. \( I \) of the Au/DNA/n-Si diodes (a) for 200 nm film layer, and (b) for 50 nm film layer.

Fig. 5. Current–voltage characteristics in logarithmic scale of the Au/DNA/n-Si diodes (a) for 200 nm film layer, and (b) for 50 nm film layer.
increasing bias voltage suggests that the time constant of interface states is decreased with applied voltage. The calculated density of interface state (Dit) of the Au/DNA/n-Si diodes is given in Table 1.

The decrease in the Dit value of the Au/DNA/n-Si diode with decreasing film thickness suggests that the electronic parameters are improved with decreasing interface state density as a result of lower coverage rate of DNA molecules in the interface.

3.4. Capacitance–voltage characteristics of the Au/DNA/n-Si diodes

Fig. 8 shows the capacitance–voltage (C–V) curves of the Au/DNA/n-Si diodes at 100 kHz. The C–V curves of the diodes were plotted in the form of C^2–V and are given in Fig. 8. The C^2 vs. V plot of the Au/DNA (50 nm)/n-Si diode gives a shorter flat region, whereas for the diode having thicker DNA thickness gives a longer flat region. This shows that the uniformity of the interface of the diode is improved with the thicker film thickness. The capacitance–voltage characteristics of the diode can be analyzed by the following relation [47],

\[
\frac{1}{C^2} = \frac{2(V_{bi} + V)}{A\varepsilon_s q N_d}
\]

(9)

where \(V_{bi}\) is the built-in potential, \(\varepsilon_s\) is the dielectric constant of semiconductor (11.7 for Si) [47] and \(N_d\) is the donor concentration. The barrier height \(\phi_h\) can be obtained by the following relation,

\[
q\phi_{b(C-V)} = qV_{bi} + kT \ln \left( \frac{N_c}{N_d} \right)
\]

(10)

where \(N_c\) is the effective density of state in the conduction band of silicon (\(N_v = 2.8 \times 10^{19} \text{ cm}^{-3}\)).

\(N_d\) and \(V_{bi}\) values for the diodes were calculated from the slope and intercept of \(C^2–V\) plot. The barrier height \(\phi_h\) values of the diodes were calculated using the obtained \(N_d\) and \(V_{bi}\) values. The \(\phi_h\) value of the diode for the thin DNA layer (50 nm) is low in comparison to that of the thicker DNA layer (200 nm). This indicates that the film thickness reduces the barrier height of the n-Si/DNA/Au diodes. The \(\phi_{b(C-V)}\) value is obtained higher than the \(\phi_{b(I-V)}\) value due to the barrier inhomogeneities of the interfacial layer thickness and distributions of the interfacial charges [48,49]. Furthermore, this also shows the I–V and C–V characteristics have different nature. Thus, the barrier heights obtained from I–V to C–V measurements are not the same [50].

4. Conclusions

The interface structure and electrical properties of Au/DNA/n-Si organic-on-inorganic structure have been investigated by atomic force microscopy, current–voltage and capacitance–voltage methods. The thickness and coverage rate of the DNA layer significantly affect electrical parameters such as the, barrier height (\(\phi_h\)), ideality factor (n), series resistance (\(R_s\)) and interface state density (\(D_{it}\)) of the Au/DNA/n-Si organic-on-inorganic structures. The interface state density for the Au/DNA/n-Si diodes was found to be \(\sim 10^{12}\) (eV cm^-2) and interface state density decreases with the decrease in film thickness. The low value of series resistance and interface state density gives the high performance of the Au/DNA/n-Si organic-on-inorganic diode for thin DNA layer thickness.

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