

# Growth and characterization of CdTe absorbers on GaAs by MBE for high concentration PV solar cells

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CdTe based II-VI absorbers are promising candidates for high concentration PV solar cells with an ideal band gap for AM1.5 solar radiation. In this study, we propose single crystal CdTe absorbers grown on GaAs substrates with a molecular beam epitaxy (MBE) which is a clean deposition technology. We show that high quality CdTe absorber layers can be grown with full width half maximum of X-ray diffraction rocking curves (XRD RC)

as low as 227 arc-seconds with 0.5 % thickness uniformity that a 2  $\mu\text{m}$  layer is capable of absorbing 99 % of AM1.5 solar radiation. Bandgap of the CdTe absorber is found as 1.483 eV from spectroscopic ellipsometry (SE) measurements. Also, high absorption coefficient is calculated from the results, which is  $\sim 5 \times 10^5 \text{ cm}^{-1}$  in solar radiation spectrum.

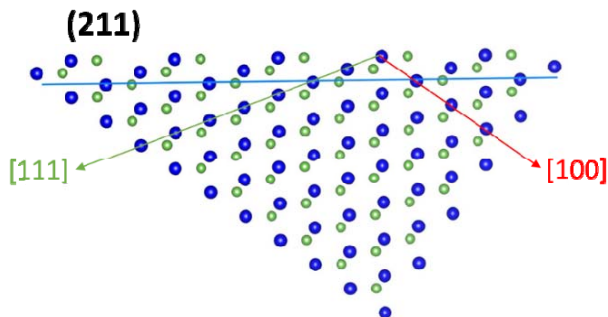
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**1 Introduction** High concentration photovoltaic (PV) solar cell systems are important in such a way that they combine less material usage with high solar conversion efficiency. III-V multi-layer structures are most widely used absorbers for high concentration PV solar cells [1]. However, structural defects such as point defects and dislocations in III-V multi-layers severely limit solar cell efficiencies which are usually attributed to the dominant covalent bond characteristic between Ga and As atoms [1]. For II-VI semiconductor absorbers materials, on the other hand, optical properties are expected to be less affected from the dislocations and other structural defects which is believed to be related with higher ionic character of bonding between the II and VI atoms than that of III-V atoms [2]. CdTe based II-VI absorbers are promising candidates for high concentration PV solar cell systems. The efficiency of the polycrystalline CdTe solar cell is reached 21.5 % [1] while theoretical limit is  $\sim 32\%$  [2]. Low solar cell efficiencies of the polycrystalline CdTe based solar cells are correlated with the low open circuit voltages ( $V_{oc}$ ). This problem is thought to be related with structural defects in the polycrystalline CdTe absorbers and

interfaces [3]. In the early 1990s, there was a brief interest in CdTe single crystal solar cell devices [3]. However, solar cell community lost interest in CdTe single crystal solar cells due to high production costs and small area of the suitable substrates. In the late 2000s, a new approach to form single and multi-junction single crystal CdTe solar cell devices is reported [3].

Dislocations are the structural defects in the CdTe absorber and hence act as trap centers for the free electrons [4]. These trap zones acts like recombination centers for the electrons and holes which they greatly reduce the solar cell efficiency. In this study we propose an environment friendly method for single crystal CdTe(211) absorber deposition on GaAs(211)B substrates by employing a high purity CdTe source in an MBE system. Surface structure and growth mode were monitored and analyzed *in situ* by reflection high energy electron diffraction (RHEED) system. In order to evaluate crystal quality high resolution X-ray diffraction (XRD) rocking curve (RC) measurements were performed around surface symmetric (422) reflections from [0-11] azimuth. Electronic properties of the CdTe absorber were obtained with SE technique. Thickness uniformity of the CdTe absorbers

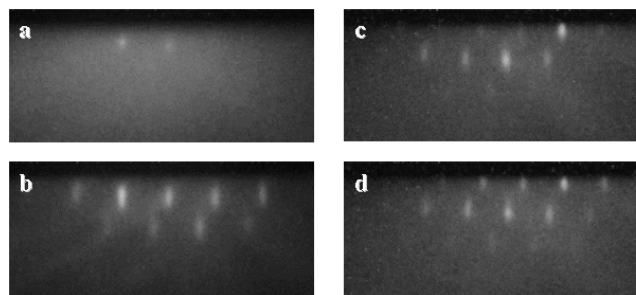
over the wafer were evaluated from the interference fringes at the transmission spectra in near infrared (NIR) region.



**Figure 1** Ball model of the (211) surface of the GaAs.

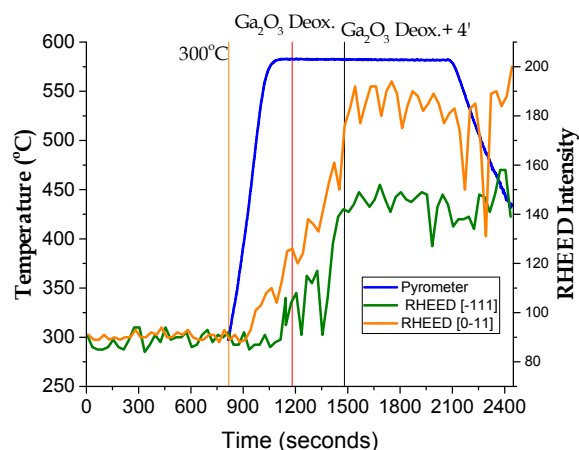
**2 MBE growth** We grew CdTe thin absorber layers on  $2 \times 2 \text{ cm}^2$  GaAs(211)B epi-ready substrates by MBE with 99.99999% (7N) purity CdTe material. Lattice mismatch between the CdTe and GaAs is 14.6%. High index (211) surface contains (111) and (100) crystal planes as they form step edges at the surface. Step edge geometry of the (211) surface has advantage of having uniform energetically favorable nucleation sites [5,6] (Fig. 1). (211) surface orientation plays an important role to suppress twinning and other structural defects during the growth of the CdTe absorbers [7]. Ideal epitaxial (211) layers grow on GaAs(211)B substrates with  $\sim 3^\circ$  tilting towards [-111] azimuth which allows a better lattice parameter match between the (4-2-2) planes of the CdTe and GaAs [8]. The (4-2-2) plane is perpendicular to the (111) orientation and interplanar distance of (4-2-2) is proportional to the bond distance in (111) orientation. In this context, a better lattice match in (4-2-2) orientation leads to a better match of the (111) planes of GaAs and CdTe [9]. Also, surface termination (As or Ga) of the substrate in the polar (211) surface may affect the surface morphology and chemical structure of the protective oxide layer of the epi-ready wafers.

Protective oxide layer of GaAs epi-ready wafers were thermally removed under As overpressure at  $585^\circ\text{C}$  surface temperature which were measured by pyrometer. The sample was then cooled to the growth temperatures as the temperature was monitored with a non-contact thermocouple at the back side of the sample. Growth was performed in three steps; initially a nucleation layer was deposited at  $365^\circ\text{C}$  which was followed by anneal at  $525^\circ\text{C}$  for 5 minutes. Finally, the growth of the absorber layer was initiated at  $460^\circ\text{C}$  with a  $4.1 \times 10^{-10}$  Torr background pressure.



**Figure 2** *In situ* RHEED patterns from [0-11] direction during the growth process; (a) before, and (b) after deoxidation of the GaAs(211)B surface, (c) CdTe initiation layer growth, (d) CdTe absorber layer growth.

**3 Results** Deoxidation, nucleation, annealing and absorber layer growth of surface was *in situ* monitored with RHEED. Evolution of the RHEED patterns during deoxidation were observed from [0-11] azimuth with a CMOS detector based system and diffraction spot intensities were recorded. RHEED pattern from the protective oxide layer of the GaAs substrate is shown in Fig. 2a, which indicates an amorphous oxide structure. After the thermal deoxidation of the GaAs substrate, RHEED pattern exhibit long streaks which points that surface is atomically smooth and ready for growth (Fig. 2b). In Fig. 2c and d, nucleation layer and absorber CdTe layer RHEED patterns are given, respectively. It is seen from the figures that rough growth of the nucleation layer turns into smoother surface with annealing. The growth of the CdTe absorber layer at this surface exhibits RHEED pattern of a highly crystalline and atomically smooth surface.



**Figure 3** *In situ* RHEED pattern spot intensity evolution with respect to surface temperature and temperature from [0-11] and [-111] azimuths.

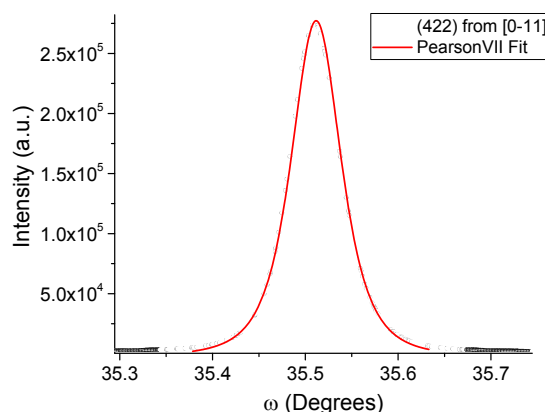
Recorded RHEED spot intensities during deoxidation of the GaAs surface is shown in Fig. 3. RHEED spot intensities were recorded from two different surface azimuths [0-11] and [-111]. RHEED spot intensity changes were observed during the surface temperature change from

300 °C to 585 °C. After that point, RHEED pattern has shown no pattern and intensity changes. First RHEED spot intensity increase is related with the As<sub>2</sub>O<sub>3</sub>, As<sub>2</sub>O<sub>5</sub> and Ga<sub>2</sub>O desorption from the surface while the second one is related to more tightly bonded Ga<sub>2</sub>O<sub>3</sub> desorption from the GaAs surface [10].

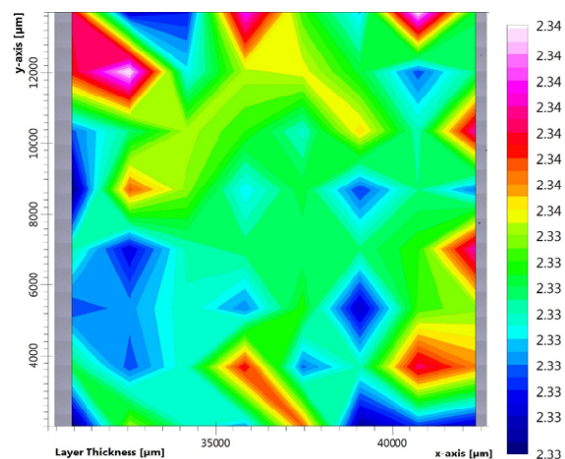
Crystal quality of the CdTe absorber layer is determined by employing XRD technique. High resolution four-crystal XRD RC measurements are performed around the CdTe (422) surface reflection from [0-11] azimuth. A measurement result is shown in Fig. 4. RC peak is modelled with a PearsonVII curve fit which has adjusted R<sup>2</sup> value of 0.9994. Full width half maximum (FWHM) of the RC is 227.4 ± 0.5 arc-seconds. Threading dislocation density of the CdTe absorber is calculated from the FWHM of the CdTe (422) reflection as 1.28 × 10<sup>8</sup> cm<sup>-2</sup> [11].

Thickness uniformity of the CdTe absorber layer is determined from the transmission map which was obtained with Fourier transform infrared (FTIR) spectroscopy system. Thickness is calculated by evaluation of the interference fringes in near infra-red region (10500-650 cm<sup>-1</sup>) of the transmission spectra which were taken at 7 × 7 points in an approximately 11.5 × 11.5 mm<sup>2</sup> zone of the CdTe absorber layer. Evaluated thickness map from the CdTe absorber layer is shown in Fig. 5. The deviation in the CdTe absorber layer thickness was ±0.005 μm with 2.335 μm average layer thickness.

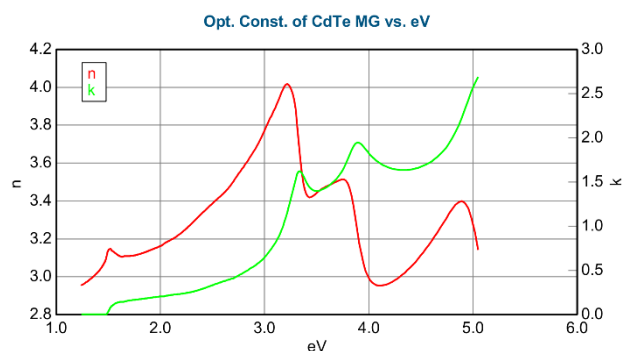
The optical properties of the CdTe absorber layer were measured with SE technique. Dielectric functions of the CdTe absorber layer is simulated with a multi-oscillator model. Thickness of the layer was calculated as 2.2940 ± 0.0004 μm which is in good agreement with the thickness obtained from the FTIR measurements. Calculated refractive index and extinction coefficient of the CdTe absorber layer is shown in Fig. 6. Sharp increase in the extinction coefficient around the ~1.5 eV is remarkable and it is related to the photon absorption of the material. Additionally, absorption coefficient is calculated from the extinction coefficient in order to determine the band gap of the CdTe absorber layer and light absorbing properties of the CdTe layer (Fig. 7). Absorption coefficient of the CdTe layer is found as ~5 × 10<sup>5</sup> cm<sup>-1</sup> in NIR and UV-VIS region. Also, the calculated bandgap of the CdTe absorber layer is found to be 1.483 eV at room temperature. Both absorption coefficient and bandgap of the CdTe absorber layer are indicators of the highly quality single crystal absorber layer [4].



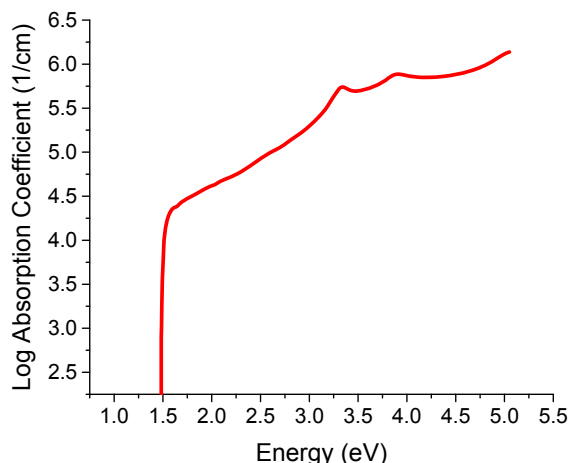
**Figure 4** Four crystal high resolution XRD RC measurement around the CdTe (422) symmetric reflection from [0-11] azimuth.



**Figure 5** Thickness map of the CdTe absorber layer of approximately 11.5 × 11.5 mm<sup>2</sup> area.



**Figure 6** Refractive index (n) and extinction coefficient (k) of the CdTe absorber layer.



**Figure 7** Absorption coefficient of the CdTe absorber layer as function of photon energy.

**4 Conclusions** In this study, we show that highly crystalline CdTe absorber layer can be grown on GaAs substrates with XRD RC FWHM 227 arc-seconds and  $1.28 \times 10^8 \text{ cm}^{-2}$  dislocation densities at about 2.3  $\mu\text{m}$  thickness. Low dislocation density value suggests very low trap centers in CdTe absorber layer hence possible high solar cell efficiencies with this layer. Also thickness uniformity of the CdTe absorber layer is found as 0.5% for 2.335  $\mu\text{m}$  layer. On the other hand, absorption coefficient for this layer is calculated as  $\sim 5 \times 10^5 \text{ cm}^{-1}$  for solar radiation spectrum which is in good agreement with bulk value [4]. In addition, bandgap of the CdTe layer is found as 1.483 eV. Therefore, MBE-grown single crystal CdTe absorbers have great potential for high concentration PV solar cell devices with low dislocation densities grown by a clean production method.

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